



Ministry of Electronics &
Information Technology,
Government of India



Indian Nanoelectronics Users' Programme – Idea to Innovation
A project of MeitY, Govt. of India



INUP- i2i Hands-on Training Workshop on Fabrication and Characterization of Spintronic Devices
February 20-29, 2024, IIT Bombay
Programme Schedule

Tuesday (February 20, 2024) Venue: Conference Room, 4th Floor, VMCC		
08:30 - 09:20 AM	Registration	
09:20 - 09:30 AM	Welcome address	
09:30 - 10:30 AM	Overview of INUP-i2i	Dr. K. Nageswari
10:30 - 11:30 AM	Overview of IITBNF infrastructure	Dr. Deepti Rukade
	Tea Break	
12 Noon - 01:00 PM	Fabrication and characterization of AMR devices	Ms. Akanksha Chouhan
01:00 - 02:00 PM	Lunch Break	
02:00 - 07:00 PM	Lab session	
Wednesday (February 21, 2024) Venue: Conference Room, 4th Floor, VMCC		
09:30 - 10:30 AM	Introduction to Spintronics	Prof. Ashwin Tulapurkar
10:30 AM -12 Noon	Lithography	Prof. Anil Kottantharayil
	Tea Break	
12:15 PM - 01:15 PM	Presentation by participants	
01:15 - 02:15 PM	Lunch Break	
02:15 - 06:00 PM	Lab session	
Thursday (February 22, 2024) Venue: Conference Room, 4th Floor, VMCC		
09:30 AM - 01:00 PM	ESR Workshop	
01:00 - 02:00 PM	Lunch Break	
02:00 - 06:00 PM	Lab session (ESR Workshop)	
Friday (February 23, 2024) Venue: Conference Room, 4th Floor, VMCC		
09:30 - 10:30 AM	Spins in biological and biomimetic systems	Prof. Swaroop Ganguly
10:30 -11:30 AM	Magnetization dynamics	Mr. Rachit Pandey
	Tea Break	
11:30 AM - 1:00 PM	Thin Film Depositions	Prof. Apurba Laha
01:00 - 02:00 PM	Lunch Break	
02:00 - 06:00 PM	Lab session	

Monday (February 26, 2024)		
Venue: Conference Room, 4th Floor, VMCC		
09:30 - 10:30 AM	Embedded Non-Volatile Memory Technologies	Prof. Veeresh Deshpande
10:30 - 11:30 AM	Low Temperature Alumina as a passivation layer on GEETs	Mr. Nama Premsai
	Tea Break	
12:15 PM - 1:00 PM	Presentation by participants	
01:00 - 02:00 PM	Lunch Break	
02:00 - 06:00 PM	Lab session	
Tuesday (February 27, 2024)		
Venue: Conference Room, 4th Floor, VMCC		
09:30 - 10:30 AM	Neuromorphic Computing with Spintronic Domain-wall Devices	Prof. Debanjan Bhowmick
10:30 - 11:30 AM	From Silicon wafer to Chip: Semiconductor device processing- A Holistic view	Dr. Bikash Dev Choudhury
	Tea Break	
12 Noon - 1:00 PM	Spin LEDs	Prof. Dipankar Saha
01:00 - 02:00 PM	Lunch Break	
02:00 - 06:00 PM	Lab session	
Wednesday (February 28, 2024)		
Venue: Conference Room, 4th Floor, VMCC		
09:30 - 10:30 AM	Neuromorphic Computing with Spintronic Oscillators	Prof. Debanjan Bhowmick
	Tea Break	
11:00 AM - 01:00 PM	Presentation by participants	
01:00 - 02:00 PM	Lunch Break	
Wednesday (February 28, 2024)		
Venue: Collaborative Classroom EE-G401, 4th floor, Girish Gaitonde Building		
02:30 - 03:30 PM	QATK and how it can be used for Spintronics	Mr. Troels Markussen
03:30 - 06:30 PM	Lab session	
Thursday (February 29, 2024)		
Venue: Conference Room, 4th Floor, VMCC		
09:30 -10:30 AM	Simulation of edge roughness via NEGF simulations	Prof Ashwin Tulapurkar
10:30 – 11:30 AM	MCQ Test	
	Tea Break	
12:00 Noon – 1:00 PM	Q & A on Thin Film Depositions	Prof. Apurba Laha
1:00 PM - 1:30 PM	Feedback session & Certificate Distribution	
	Luch Break	